

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	gpio and dispalay and power	EPO; DERWENT	OR	OFF	2006/04/11 07:50
L2	3	gpio and display and power	EPO; DERWENT	OR	OFF	2006/04/11 07:51
L3	2	gpio and display and power	JPO	OR	OFF	2006/04/11 07:52
L4	737	gpio and display and power	US-PGPUB; USPAT	OR	OFF	2006/04/11 07:53
L5	0	gpio and display with (selective dynamic adaptive) near power	US-PGPUB; USPAT	OR	OFF	2006/04/11 07:53
L6	39	display with (selective dynamic adaptive) near power	US-PGPUB; USPAT	OR	OFF	2006/04/11 07:53
L7	25	("5859594").URPN.	USPAT	OR	OFF	2006/04/11 07:57
L8	6	("4481382" "5491491" "5854984" "5859594" "5884155" "5924044").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 07:59
L9	3	("6032021").URPN.	USPAT	OR	OFF	2006/04/11 08:00
L10	36	("5355490").URPN.	USPAT	OR	OFF	2006/04/11 08:04
L11	16	("5355490" "5612520" "5640176" "5719618" "5752048" "5758172" "5822599" "5832285" "5935244" "5938772" "5978923" "5987613" "6006335" "6076133" "6205318" "6289466").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 09:59
L12	52869	message and display with (control switch\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 10:00
L13	13195	message same display with (control switch\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 10:00
L14	1516	message same display with (control switch\$3) same power	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 10:01
L15	228	14 and "455"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 10:01
L16	1110	message same display with (control (switch\$3 near off)) same power	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 10:02
L17	154	15 and 16	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 10:02

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L18	2	17 and 455/522.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 10:02
L19	16	17 and short adj. message	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/11 10:03

PAT-NO: JP02004046324A

DOCUMENT-IDENTIFIER: JP 2004046324 A

**TITLE: INFORMATION PROCESSOR WITH STANDBY
MODE, AND STANDBY
MODE STARTING METHOD AND STANDBY MODE
CANCELING METHOD
FOR THE SAME**

PUBN-DATE: February 12, 2004

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INT-CL (IPC): G06F001/30, G06F001/26 , G06F001/32

ABSTRACT:

PROBLEM TO BE SOLVED: To provide an information processing device that shortens time for recovery from a standby mode and extends the duration of a battery by reducing power consumption in the standby mode.

SOLUTION: The information processor comprises a power supply control unit 2 for controlling power supply to each device in a standby mode, and a power supply switching unit 10 for switching supply and cutoff of source power to each device 1, 5, 6, 8, 9 according to an instruction from the power supply control unit 2. Upon detection of a start of the standby mode, after contents of registers 1b, 5a and 6b and a frame buffer 6a are stored in a storage circuit 7, power supply to the peripheral device 8 and the display device 9 is stopped, and then power supply to the main control unit 1 except for GPIO 1a, the peripheral device control circuit 5 and the display control circuit 6 is stopped.

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